

Rigo G. Dicochea

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EDUCATION

- **Ph.D., Computer Engineering**, University of California at Santa Cruz-
Expected Jun. 2013
- **M.S., Computer Engineering**, University of California at Santa Cruz-
Expected Jun. 2010
- **B.S., Computer Engineering**, University of California at Santa Cruz-
Jan. 2006

RELATED EXPERIENCE

-HARDWARE DESIGN/SIMULATION

- Lead project engineer on high performance out-of-order CPU(SCOORE)
 - Designed and tested complete execution engine
- Developed Adaptive Optics System utilizing FPGA's in conjunction with the Center for Adaptive Optics (CfAO)
- Implemented an advanced matrix multiplication on FPGA for use on CFAO project
- Analyzed the resources and timing of hardware through simulations carried out through Synopsys Verilog Compiler Simulator (VCS)
- Participated in out of state conference detailing my work on adaptive optics
- Utilized StateCAD and ModelSIM for design and analysis of hardware architecture and timing

TECHNICAL EXPERTISE

- **Software:** Synplify Pro, VCS, dc_shell, Hardware Schematics, FreeBSD, Linux, UNIX, WindowsXP, Vista, Windows 7
- **Languages:** System Verilog, C, C++

EMPLOYMENT HISTORY

- **Teaching Assistant:** UC Santa Cruz- Santa Cruz, CA.95060 6/08-present
- **CFAO Intern:** UC Santa Cruz- Santa Cruz, CA.95060 6/06-12/06
- **Networking Course Grader:** UC Santa Cruz- Santa Cruz, CA.95060 1/03-6/03
- **Math Tutor:** Cabrillo College- Aptos, CA. 95060 6/02 - 9/02
- **Data Entry Clerk:** Watsonville Hospital- Watsonville, CA. 95076 6/01-10/01
- **Youth Counselor:** Watsonville Youth Center- Watsonville, CA. 95076 5/00-9/00