

UNIVERSITY OF CALIFORNIA  
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**OPTIMISTIC-CHECKER TANDEM PROCESSOR DESIGN: EXPLOITING  
DESIGN COMPLEXITY TO EXPOSE NEW OPPORTUNITIES FOR  
PROCESSOR OPTIMIZATION**

A dissertation submitted in partial satisfaction of the  
requirements for the degree of

DOCTOR OF PHILOSOPHY

in

COMPUTER ENGINEERING

by

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June 2007

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## **Acknowledgments**

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## **Chapter 4**

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## **Chapter 5**

### **Related Work**

This chapter reviews previous work that serves as a basis for the principles and insights that this thesis is based upon, or that is most closely related to sections of this work. We segregated the study of published material related to this work into three sections; first we review related approach to efficient instruction scheduling, second explore published work on the modeling and measurement of design complexity, and finally we review work related to architectural pruning.

## **Chapter 6**

### **Conclusions**